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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
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Mutsuko Hatano

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EXAMINER

MOON, SEOKYUN

ART UNIT

PAPER NUMBER

2629

MAIL DATE

DELIVERY MODE

10/23/2008

PAPER

**Please find below and/or attached an Office communication concerning this application or proceeding.**

The time period for reply, if any, is set in the attached communication.

## **DETAILED ACTION**

### ***Response to Arguments***

1. As a preliminary matter, Examiner thanks the Applicant for submitting detailed diagram and explanation regarding the concept of the technologies disclosed in the instant Application.
2. Prior to the discussion regarding the Applicants' arguments, Examiner respectfully submits that the subject matter of the instant Application might be different or distinguishable from the prior arts of record, but such subject matter is not presented and/or disclosed in the claims specifically enough to distinguish the instant invention from the prior arts of record.
3. The Applicant [pg 6 last paragraph - pg 7 first paragraph] argued, *"It is not articulated why at the time of the invention was made, one of ordinary skill in the art would have selectively used roughly-band-shaped-crystal silicon films having grain boundaries as channels of said active elements, each of the grain boundaries of the roughly-band-shaped-crystal silicon films being continuous in generally one direction to form active elements in each of said shift registers, said digital-analog converter, and said buffer circuit, while selectively used granular polysilicon films having loop-like grain boundaries as channels of said active elements to form active elements in said sampling circuit"*.

However, Examiner respectfully submits that the combination of the prior arts disclosed in the Office Action mailed on April 24, 2008 was based on the idea of using Taketomi's concept of forming active elements, i.e. thin film transistors, to build the active elements included in the

Art Unit: 2629

shift register, the digital-to-analog converter, and the buffer circuit **as well as the active elements included in the sampling circuit**. As disclosed in the previous Office Action, Taketomi teaches a concept of forming active elements, i.e. thin film transistors, by using a roughly-band-shaped-crystal silicon films having grain boundaries as channels of the active elements, wherein each of the grain boundaries of the roughly-band-shaped-crystal silicon films are continuous in generally one direction and the thin film transistors have a direction of movement carriers therein in a direction of the grain boundaries **and the thin film transistors use granular polysilicon films having loop-like grain boundaries as channels of the thin film transistors**. In other words, Taketomi teaches one type of thin film transistor employing a silicon film **which is both a roughly-band-shaped crystal silicon film and a granular polysilicon film**. Accordingly, building the active elements, i.e. thin film transistors, included in each of the stages of the driver of Kaise, by using the concept of Taketomi would teach the claim limitation.

### *Conclusion*

4. Any inquiry concerning this communication or earlier communications from the examiner should be directed to SEOKYUN MOON whose telephone number is (571)272-5552. The examiner can normally be reached on Mon - Fri (8:30 a.m. - 5:00 p.m.).

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Sumati Lefkowitz can be reached on (571) 272-3638. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Art Unit: 2629

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August 18, 2008

/S. M./

Examiner, Art Unit 2629

/Sumati Lefkowitz/

Supervisory Patent Examiner, Art Unit 2629